



NVMTS, September 29th-October 1st, 2025, Atlanta, USA



Fascinating discoveries and technological advancements in recent years have paved the way for new and competitive non-volatile memory (NVM) designs. IEEE Non-Volatile Memory Technology Symposium (NVMTS) is the eminent forum for exchanging information on non-volatile memory and we aim to create an interactive environment for discussing the most critical aspects of advanced memory technologies that will benefit everyone involved in this field, from graduate students to seasoned industry researchers. The 23rd NVMTS will be held on September 29th -October 1st, 2025, in Atlanta, USA. Dozens of invited

representatives from all branches of memory technology in both industry and academia will present state-of-the-art research and provide critical insight into future challenges for both existing and emerging technology.

The symposium will include a program with keynote speeches, invited talks, and contributed poster presentations to report the latest research in non-volatile memory. Please visit the website for more information and instructions on how to submit the abstract.

Topics:

1. Flash
2. MRAM
3. PCRAM
4. RRAM/CBRAM
5. FeRAM
6. NVDRAM
7. Selector
8. New Memory Concepts
9. Neuromorphic materials and devices

KEY DATES

Abstract Submission Deadline	Jun. 20, 2025
Notification of Acceptance	Jul. 20, 2025
Online Registration Deadline	Sep. 20, 2025
Full Paper Submission Deadline	Nov. 20, 2025

Conference Co-Chairs	Suman Datta and Shimeng Yu (Georgia Tech, USA)
Publicity Chair	Asif Khan (Georgia Tech, USA)
Technical Program Co-Chairs	Ilya Karpov (Intel, USA) and Scott Sills (Micron, USA)
Publication Chair	Eilam Yalon (Technion, Israel)

NVMTS 2025 Website: <https://nvmts2025.org/>
